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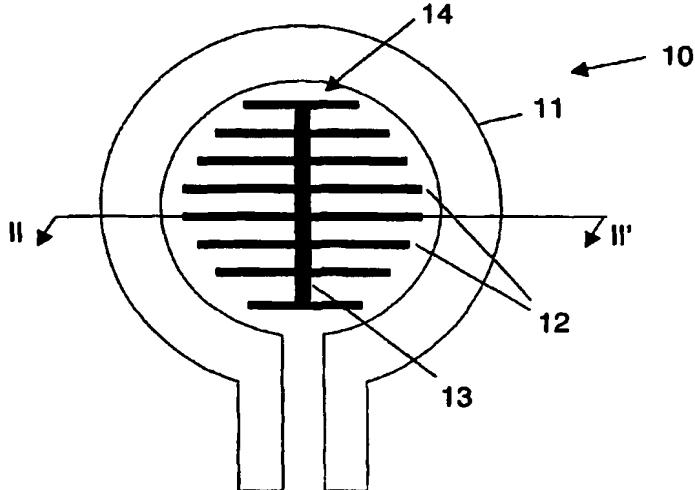
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(54) Title: INDUCTIVE AND CAPACITIVE ELEMENTS FOR SEMICONDUCTOR TECHNOLOGIES WITH MINIMUM PATTERN DENSITY REQUIREMENTS



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(57) Abstract: The present invention provides a semiconductor device (10) comprising a plurality of layers, the semiconductor device comprising: - a substrate (20) having a first major surface, - an inductive element (11) fabricated on the first major surface of the substrate, the inductive element comprising at least one conductive line, and - a plurality of tilling structures in at least one layer, wherein the plurality of tilling structures are electrically connected together and are arranged in a geometrical pattern (14) so as to substantially inhibit an induction of an image current in the tilling structures by a current in the inductive element. It is an advantage of the above semiconductor device that, by using such tilling structures, an inductive element with improved quality factor is obtained. The present invention also provides a method for providing an inductive element in a semiconductor device comprising a plurality of layers.

**Declaration under Rule 4.17:**

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